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PATENT  
TESSERA 3.3-018



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:  
Khandros et al.

Serial No.: 08/030,194

Filed: April 28, 1993

For: SEMICONDUCTOR CHIP ASSEMBLIES,  
METHODS OF MAKING SAME AND  
COMPONENTS FOR SAME

Group Art Unit: 2503

Examiner: S. Clark

Date: April 18, 1996

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GROUP 2500

Assistant Commissioner For Patents  
Washington, D.C. 20231

EXTENSION PETITION

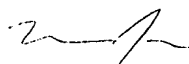
Sir:

Applicants respectfully petition for a three-month extension of time to reset the deadline for filing the Response to Official Action mailed October 27, 1995 from January 27, 1996 to and including April 27, 1996.

A check in the amount of \$900.00 is enclosed. In the event there are any fees due and owing, the Examiner is hereby authorized to charge our Deposit Account No. 12-1095.

Respectfully submitted,

LERNER, DAVID, LITTENBERG,  
KRUMHOLZ & MENTLIK

  
MARCUS J. MILLET  
Reg. No. 28,241

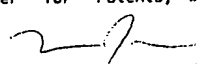
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on April 18, 1996.



Signature)

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